What is claimed is:

[Claim 1] 1. A liquid crystal display panel, comprising:

a color filter substrate;
a thin film transistor array substrate, disposed parallel to the color filter substrate, wherein the thin film transistor array substrate has a plurality of gate layer lines and a plurality of source layer lines thereon;
a plurality of spacers, disposed on the color filter substrate, wherein the spacers at least lean on one of the side edges of the gate layer lines or the source layer lines; and
a liquid crystal layer, disposed between the color filter substrate and the thin film transistor array substrate.

[Claim 2] 2. The liquid crystal display panel of claim 1, wherein the gate layer lines comprise scan lines and the source layer lines comprise data lines.

[Claim 3] 3. The liquid crystal display panel of claim 2, wherein the gate layer lines further comprise common lines.

[Claim 4] 4. The liquid crystal display panel of claim 1, wherein the spacers are disposed at the overlapping area between the gate layer lines and the source layer lines with the spacers leaning against both the gate layer lines and the source layer lines.

[Claim 5] 5. The liquid crystal display panel of claim 4, wherein adjacent spacers are joined together on the color filter substrate to form a latching structure such that a central recess section of the latching structure is able to latch onto the overlapping area between the gate layer lines and the source layer lines.

[Claim 6] 6. The liquid crystal display panel of claim 1, wherein the spacers are disposed on opposite sides of the gate layer lines with the spacers leaning against the gate layer lines as well.

[Claim 7] 7. The liquid crystal display panel of claim 6, wherein adjacent spacers are joined together on the color filter substrate to form a latching

structure such that a central recess section of the latching structure is able to latch onto the gate layer lines.

[Claim 8] 8. The liquid crystal display panel of claim 1, wherein the spacers are disposed on opposite sides of the source layer lines with the spacers leaning against the source layers as well.

[Claim 9] 9. The liquid crystal display panel of claim 8, wherein adjacent spacers are joined together on the color filter substrate to form a latching structure such that a central recess section of the latching structure is able to latch onto the source layers.

[Claim 10] 10. The liquid crystal display panel of claim 1, wherein the spacers have a cylindrical or a wall-like shape.

[Claim 11] 11. A method of fabricating a liquid crystal display panel, comprising the steps of:

providing a thin film transistor array substrate, wherein the thin film transistor array substrate has a plurality of gate layer lines and a plurality of source layer lines thereon;
providing a color filter substrate, wherein the color filter substrate has a plurality of spacers thereon; and
joining the thin film transistor array substrate and the color filter substrate together such that the spacers on the color filter substrate lean on at least one of the side edges of the gate layer lines or the source layer lines of the thin film transistor array substrate.

[Claim 12] 12. The method of claim 11, wherein the step of forming the color filter substrate comprises:

providing a glass substrate;
forming a black matrix over the glass substrate such that the black matrix encloses a plurality of openings on the glass substrate;
forming a plurality of color filtering units inside the openings enclosed by the black matrix; and
forming spacers on the black matrix.

[Claim 13] 13. The method of claim 12, wherein the step of forming the spacers comprises:

forming an organic photosensitive material layer over the glass substrate; and patterning the organic photosensitive material layer.

[Claim 14] 14. The method of claim 12, wherein after forming the spacers, further comprises post-baking the spacers.